

Abstract of the Disclosure

A linked list is implemented in hardware. Various registers within the linked list are writeable until a control register is written, rendering the registers read-only. A

- 5 computer peripheral includes the hardware linked list to provide a list of capabilities to a querying device. The linked list can be built, modified, or disabled by low level software, and then locked so that it appears as read-only to higher level software such as an operating system or device driver.

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